

File 347:JAPIO Nov 1976-2004/Apr(Updated 040802)
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 File 350:Derwent WPIX 1963-2004/UD,UM &UP=200454
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 File 348:EUROPEAN PATENTS 1978-2004/Aug W03
 (c) 2004 European Patent Office
 File 349:PCT FULLTEXT 1979-2002/UB=20040819,UT=20040812
 (c) 2004 WIPO/Univentio

Set	Items	Description
S1	93	AU=DAYAN R?
S2	142	AU=KERN E?
S3	14083	USB OR UNIVERSAL()SERVICE? ?() (BUS OR PORT? ? OR BUSES OR - BUSED OR Busing OR BUSSE? ? OR BUSSING OR SUBBUS? OR MULTIBUS? OR DATABUS?)
S4	3	S1:S2 AND S3

? t4/9/1-2;t4/5/3

4/9/1 (Item 1 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
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015800725 **Image available**
 WPI Acc No: 2003-862928/200380
 XRPX Acc No: N03-688709

USB interface for connecting personal computer with peripherals e.g. joystick, provides commands to USB adapter to perform polling, for determining whether wake-on-LAN stimulus is received by adapter during power supply period

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)
 Inventor: BURKE T C; CROMER D C; **DAYAN R A ; KERN E R**
 Number of Countries: 001 Number of Patents: 001
 Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6622178	B1	20030916	US 2000611788	A	20000707	200380 B

Priority Applications (No Type Date): US 2000611788 A 20000707

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6622178	B1		8 G06F-003/00	

Abstract (Basic): US 6622178 B1

NOVELTY - A power supply periodically supplies power to a **USB** local area network (LAN) adapter (18) which receives command from a controller through an interface (20). The controller controls the power supply and performs polling of **USB** adapter to determine whether wake-on-LAN stimulus is received by the adapter during power supply period.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (1) computer system; and
- (2) method for activating computer system.

USE - **USB** interface used for connecting personal computer system with peripherals such as pointing device, joystick, modem and LAN adapters.

ADVANTAGE - Enables polling of **USB** peripherals such as **USB** adapter without requiring continuous supply of power to the adapter and without using standard portions of the **USB** interface.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of the computer system having **USB** interface.

computer system (10)
CPU (11)
memory (12)
bus (13)
 USB LAN adapter (18)
 USB host interface (20)
pp; 8 DwgNo 1/4
Title Terms: INTERFACE; CONNECT; PERSON; COMPUTER; PERIPHERAL; JOYSTICK;
COMMAND; PERFORMANCE; POLL; DETERMINE; WAKE; LAN; STIMULUS; RECEIVE;
POWER; SUPPLY; PERIOD
Derwent Class: T01
International Patent Class (Main): G06F-003/00
File Segment: EPI
Manual Codes (EPI/S-X): T01-C02B; T01-C07C5; T01-L01

4/9/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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015394538 **Image available**
WPI Acc No: 2003-456679/200343
XRPX Acc No: N03-363183

**Host computer system has universal serial bus hot plug detector which
initiates system management interrupt signal on detecting connection of
USB device to USB port in USB hub**

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)
Inventor: BURKE T C; CROMER D C; DAYAN R A ; KERN E ; SPRINGFIELD R S
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20030056051	A1	20030320	US 2001957253	A	20010920	200343 B

Priority Applications (No Type Date): US 2001957253 A 20010920

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20030056051	A1	7	G06F-013/00	

Abstract (Basic): US 20030056051 A1

NOVELTY - A universal serial bus (**USB**) hot plug detector (20)
connected between a **USB** hub and a **USB** host controller (18),
initiates a system management interrupt (SMI) signal to the host
computer system, on detecting the connection of the **USB** device to an
USB port in the **USB** hub.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for a
method for selectively connecting universal serial bus device to host
computer system.

USE - Host computer system.

ADVANTAGE - Securely monitors the connection of the **USB** device to
the **USB** port, without initial notification of the operating system.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of
the computer architecture.

USB host controller (18)

USB hot plug detector (20)

pp; 7 DwgNo 1/3

Title Terms: HOST; COMPUTER; SYSTEM; UNIVERSAL; SERIAL; BUS; HOT; PLUG;
DETECT; INITIATE; SYSTEM; MANAGEMENT; INTERRUPT; SIGNAL; DETECT; CONNECT;
DEVICE; PORT; HUB
Derwent Class: T01; T04; W04
International Patent Class (Main): G06F-013/00

File Segment: EPI

Manual Codes (EPI/S-X): T01-C07C4; T01-F05B2; T01-G05C; T04-L05; W04-K07;
W04-K08

4/5/3 (Item 1 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00957048 **Image available**

DATA PROCESSING SYSTEM AND METHOD FOR PASSWORD PROTECTING A BOOTING ORDER
OF BOOT DEVICES

SYSTEME ET PROCEDE DE TRAITEMENT DE DONNEES POUR PROTEGER PAR MOT DE PASSE
UN DISPOSITIF D'INITIALISATION

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200291201 A1 20021114 (WO 0291201)

Application: WO 2001US47732 20011212 (PCT/WO US0147732)

Priority Application: US 2001847085 20010502

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL
TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-015/16

International Patent Class: G06F-009/00; G06F-001/24; G06F-011/30

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 4010

English Abstract

The process depicted in Figure 2A begins at block (100), in response to power on or power-on-reset of a computer system. The process then proceeds with (104), the CPU begins execution of BIOS software. As depicted at block (106), the BIOS software determines whether a request to enter the BIOS configuration routine has been received. A user may request to enter the BIOS configuration routine, for example, by depressing a designated function key (e.g. F1) of keyboard. If no request to enter the BIOS configuration routine is received, the process passes to block (120). However, if a BIOS configuration request is received, the BIOS software prompts the user to enter a configuration password (108).

Entry to the BIOS configuration routine is preferably password protected to prevent unauthorized changes to the order (priority) in which boot devices are checked for a bootable operating system at system startup. The BIOS software then scans through the list of boot devices in sequence from the highest priority device to the lowest priority device to locate the highest priority device from which computer system can boot an operating system. Thus, at block 8122), the BIOS software selects the highest priority boot device that has not been checked for an operating system from the list of possible boot devices. Next, block (124) illustrates the BIOS software determining whether or not the selected boot device is capable of booting an operating system. If not, the process returns to block (122). If, however, a determination is made at block (124) that the selected boot device is capable of booting the computer system, the process passes to block (126), which depicts the BIOS software determining whether or not a correct entry of a password is required to boot from the selected boot device. Process (128), then determines at block (130) whether or not the correct password was entered. If a determination is made at block (130) that the correct password was obtained from the boot device, the process passes to block (132), which illustrates the BIOS software booting an operating system utilizing the selected boot device. Thereafter, processing continues under the control of the operating system at block (134).

French Abstract

Le procede decrit dans la figure 2A commence a la case (100) en reponse a l'alimentation ou la remise a l'etat initial lors de l'alimentation d'un systeme informatique. Le procede procede ensuite avec (104), l'UC commence l'execution d'un logiciel BIOS. Tel que le decrit la case (106), le logiciel BIOS determine si une demande d'entree dans le programme de configuration BIOS a ete recue. Un utilisateur peut demander d'entrer dans le programme de configuration BIOS, par exemple, en appuyant sur une touche de fonction designee (par exemple, F1) du clavier. Si aucune demande d'entree dans le programme de configuration BIOS n'est recue, le procede passe a la case (120). Toutefois, si une demande de configuration BIOS est recue, le logiciel BIOS invite l'utilisateur a entrer un mot de passe de configuration (108). L'entree dans le programme de configuration BIOS est de preference protegee par un mot de passe afin d'empecher les changements non autorises de l'ordre (priorite) dans lequel les dispositifs d'initialisation sont controles pour un systeme de commande initialisable lors du demarrage du systeme. Le logiciel BIOS passe alors en revue la liste des dispositifs d'initialisation en sequence depuis le dispositif ayant la plus haute priorite jusqu'au dispositif ayant la moins haute priorite afin de localiser le dispositif de la plus haute priorite a partir duquel un systeme informatique peut initialiser un systeme d'exploitation. Ainsi, dans la case (122) le logiciel BIOS selectionne le dispositif d'initialisation de la plus haute priorite n'ayant pas ete controle pour un systeme d'exploitation, dans la liste des dispositifs d'initialisation possible. Ensuite, la case (124) illustre le logiciel BIOS determinant si oui ou non le dispositif d'initialisation selectionne est capable d'initialiser un systeme d'exploitation. Sinon, le procede revient a la case (122). Cependant, si il est determine au niveau de la case (124) que le dispositif d'initialisation selectionne est capable d'initialiser le systeme informatique, le procede passe a la case (126), laquelle decrit le logiciel BIOS determinant si oui ou non une entree correcte d'un mot de passe est necessaire pour initialiser a partir du dispositif d'initialisation selectionne. Le processus (128) decrit l'interrogation par logiciel BIOS d'un mot de passe de dispositif dans une memoire remanente se trouvant dans le dispositif d'initialisation selectionne. Le logiciel BIOS determine alors au niveau de la case (130) si oui ou non le mot de passe correct a ete entre. Si il est determine, au niveau de la

case (130), que le mot de passe correct a ete obtenu du dispositif d'initialisation, le procede passe a la case (132), laquelle illustre l'initialisation par logiciel BIOS d'un systeme d'exploitation utilisant le dispositif d'initialisation selectionne. Ensuite, le traitement continue sous la commande du systeme d'exploitation au niveau de la case (134).

Legal Status (Type, Date, Text)

Publication 20021114 A1 With international search report.

Examination 20030501 Request for preliminary examination prior to end of
19th month from priority date

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